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			PLICANT	First Named Inventor	Harry Contopanagos	84 S	
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VP-14-14-14-14-14-14-14-14-14-14-14-14-14-	1	"PROGRESS IN RF INDUCTORS ON SILICON - UNDERSTANDING SUBSTRATE LOSSES" by Joachim N. Burghartz - IBM Research Division, T. J. Watson Research Center, P. O. Box 218, Yorktown Heights, New York 10598 USA	
	2	IEEE Journal of Solid-State Circuits, Vol. 33, No 5, May 1998 - the "Stanford Paper" - "ON-CHIP SPIRAL INDUCTORS WITH PATTERNED GROUND SHIELDS FOR SI- BASED RF IC's - C.Patrick Yue, Student Member, IEEE, and S. Simon Wong, Senior Member, IEEE	

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